

09/854 038

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TRANSMITTAL LETTER
(General - Patent Issued)

Docket No.
BUR920000214US1

Patentee(s): **Grupp et al.**

U.S. Patent No.

6,985,842 B2

Issue Date

January 10, 2006

Title: **BIDIRECTIONAL WIRE I/O MODEL AND METHOD FOR DEVICE SIMULATION**

COMMISSIONER FOR PATENTS:

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FEB 01 2006

of Correction

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Jack P. Friedman
Signature

Dated: **01-26-2006**

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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 6,985,842 B2

DATED : January 10, 2006

INVENTOR(S) : Grupp et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1

Lines 13, 17 and 18, delete "modem" and insert -- modern--

Column 8

Line 66, delete "data-devices" and insert --devices--

Column 11

Line 9, delete "model representing" and insert -- model for representing --

Line 23, delete "part" and insert -- path --

Line 26, delete "hearing" and insert -- bearing --

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